



**R C Patel Institute of Technology, Shirpur**  
**Department of Electronics & Telecommunication**



**TIME TABLE**

**Class: TY B.TECH E & TC (B)**

**Year: 2025-26**

**Sem II**

**Class Room No. 90**

**W E F ( 02/03 /2026)**

R.N	Time	Monday	Tuesday	Wednesday	Thursday	Friday	Saturday
1	11.00 TO 11.50	ML_VPP	ML_VPP	ML_VPP	PE_SDP	RS_BVP	PE_SDP
2	11.50 TO 12.40	PE_SDP	RS_BVP	IP_PMG	ML_VPP	CN_JPP	CN_JPP
	12.40 TO 1.45	Long Recess					
3	1.45 TO 2.35	PE-B1_EL 4 ML-B2_EL 6 RS-B3_EL 5	IP - B1_EL 6 RS-B2_EL 5 CN-B3_Research Lab	RS-B1_EL 5 CN_B2_Research Lab IP-B3_EL 6	ML-B1_EL 6 IP-B2_EL 5 PE-B3_EL 4	CN-B1_Research Lab PE-B2_EL 4 ML-B3_EL 6	CN_JPP
4	2.35 TO 3.25						CI_PGP
	3.25 TO 3.40	Short Recess					
5	3.40 TO 4.30	RS_BVP	IP_PMG	CN_JPP	IP_PMG	IP_PMG	Project Hrs
6	4.30 TO 5.20	PE_SDP	Liabrary Hrs	RS_BVP	Liabrary Hrs	Project Hrs	Project Hrs
		<b>Liabrary Hours</b>					
SR		THEORY		PRACTICAL			
1	Machine Learning (ML)	Dr V V PATEL		Machine Learning (ML)	Dr V V PATEL		<b>Note: 80 % attendance is compulsory. Candidate failing to complete this will be detained</b>
2	Computer Networks (CN)	Dr J P PATIL		Computer Networks (CN)	B1, B3 -Dr J P PATIL B2- Prof P R Bhole		
3	Radiating Systems (RS)	Dr B V PATIL		Radiating Systems (RS)	Dr J B Jadhav		
4	Image Processing (IP)	Dr P M GOAD		Image Processing (IP)	B1-Dr T H Jaware B2 & B3-Dr J P PATIL		
5	Power Electronics (PE)	Dr S D PATIL		Power Electronics (PE)	Dr S D PATIL		
6	Constitution of INDIA (CI)	Dr P G Patil					
<b>Time Table Incharge</b>						<b>HOD</b>	
						 <b>HOD, E&amp;TC</b> R. C. Patel Institute of Technology Shirpur, Dist: Dhule (MS)	
	Prof P M GOAD					Prof Dr V S Patil	